

EXPEDITED PROCEDURE - EXAMINING GROUP 2822

PATENT

S/N 10/057225

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Michael Trinh

Serial No.: 10/057225

Group Art Unit: 2822

Filed: January 25, 2002

Docket No.: 303.506US4

Title: METHOD OF FABRICATING A TRANSISTOR ON A SUBSTRATE TO
OPERATE AS A FULLY DEPLETED STRUCTURE (as amended)

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Final Office Action mailed March 29, 2004, please amend the
application as follows:

Amended
w/ RCE

